



RE-EXAMINATION TIMETABLE, MARCH, 2026

BACHELOR OF TECHNOLOGY (B.Tech.)

THIRD YEAR, SEMESTER-V (For 2025-26 Batch Under Autonomy)

Timings: 10:00 a.m. to 12:00 p.m.

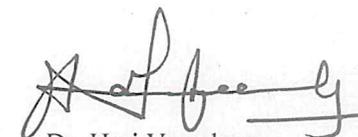
Date: 20th February, 2026

Day & Date	Mechanical Engineering	Electronics & Telecommunication Engineering	Information Technology	Computer Engineering	Computer Science & Engineering (Data Science)	Artificial Intelligence & Data Science	Artificial Intelligence & Machine Learning
Saturday, 14/03/2026	Electric Drives and Controls	Digital VLSI	DevOps	-	Econometric Modelling	-	Augmented Reality and Mixed Reality
		Machine Learning					
		Personal Finance Management					
Tuesday, 17/03/2026	Theory of Machines	Wave Theory & Radio Frequency Design	Data Warehousing and Mining	Data Warehousing and Mining	Design and Analysis of Algorithms	Recommendation Systems	-
Friday, 20/03/2026	Mechanical Measurements and Metrology	Digital Signal Processing	Artificial Intelligence	Artificial Intelligence	Computer Communication and Networks	-	-
Tuesday, 24/03/2026	Fluid Mechanics and Machinery	Analog & Digital Communication	Statistical Analysis	Automata Theory and Compiler Design	-	-	-
Friday, 27/03/2026	Industrial Electronics	Database Management System	Computer Networks	IoT-Centric Processor Organization and Architecture	-	-	-
Monday, 30/03/2026	Automotive Prime Movers	Neural Network & Fuzzy Logic	User Centered Design	Advanced Database Management Systems	-	-	-
		Linear Algebra			-	-	-

Important Note: Change if any, in the time table shall be communicated on the College website (www.djsce.ac.in).

Ms. Alka Shukla
 Controller of Examinations

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 Dr. Hari Vasudevan
 Principal